

Fig. 1b (Prior Art)

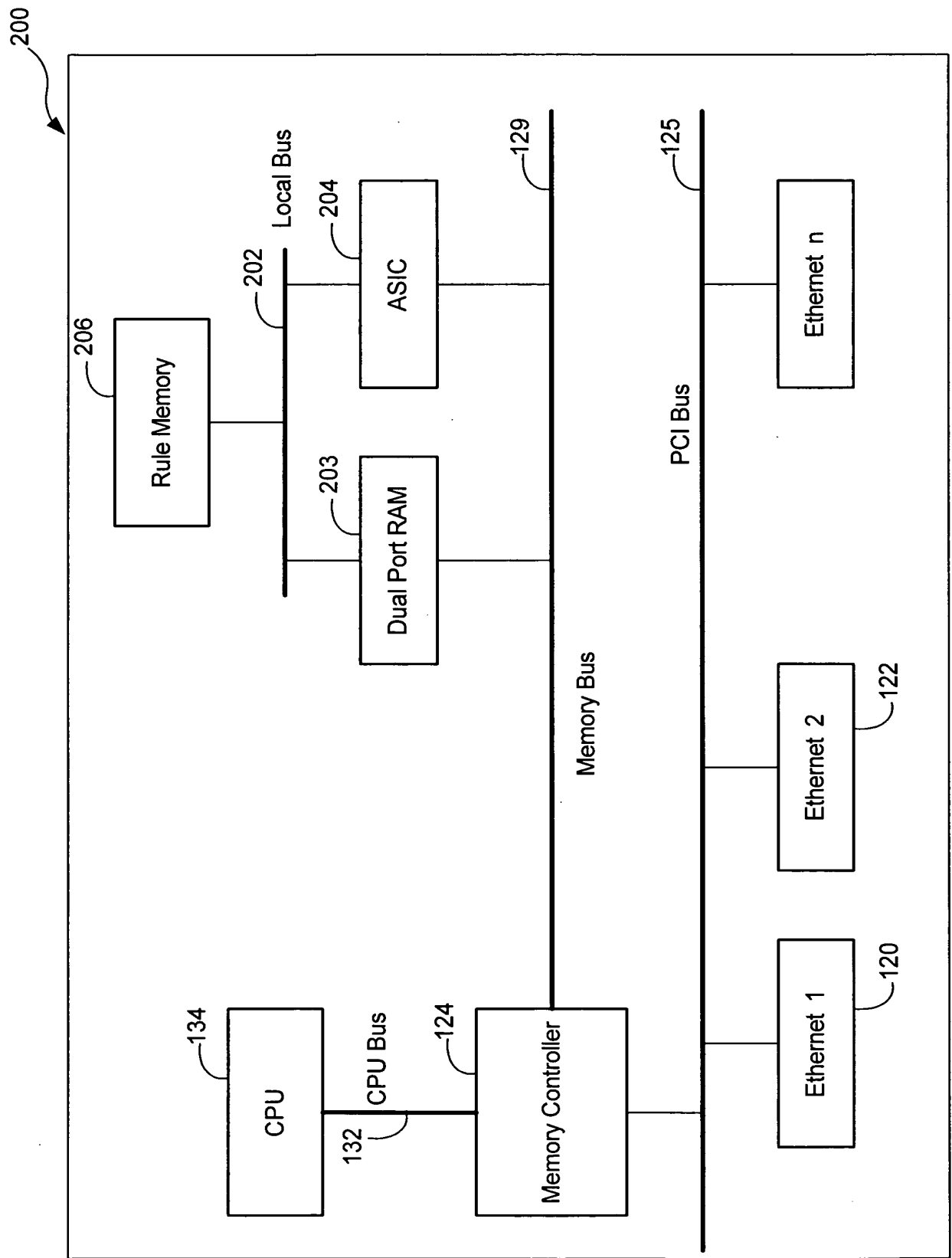


Fig. 2

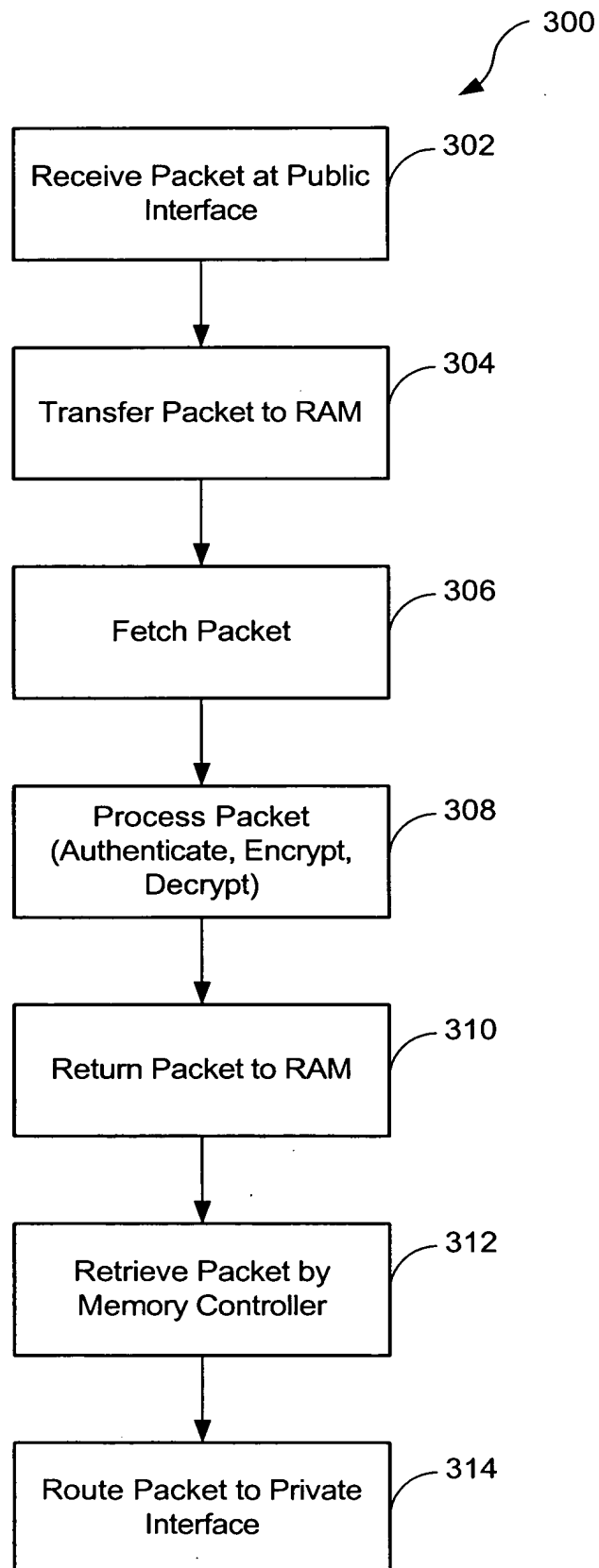


Fig. 3

Netscreen In-House Designed ASIC Block Diagram

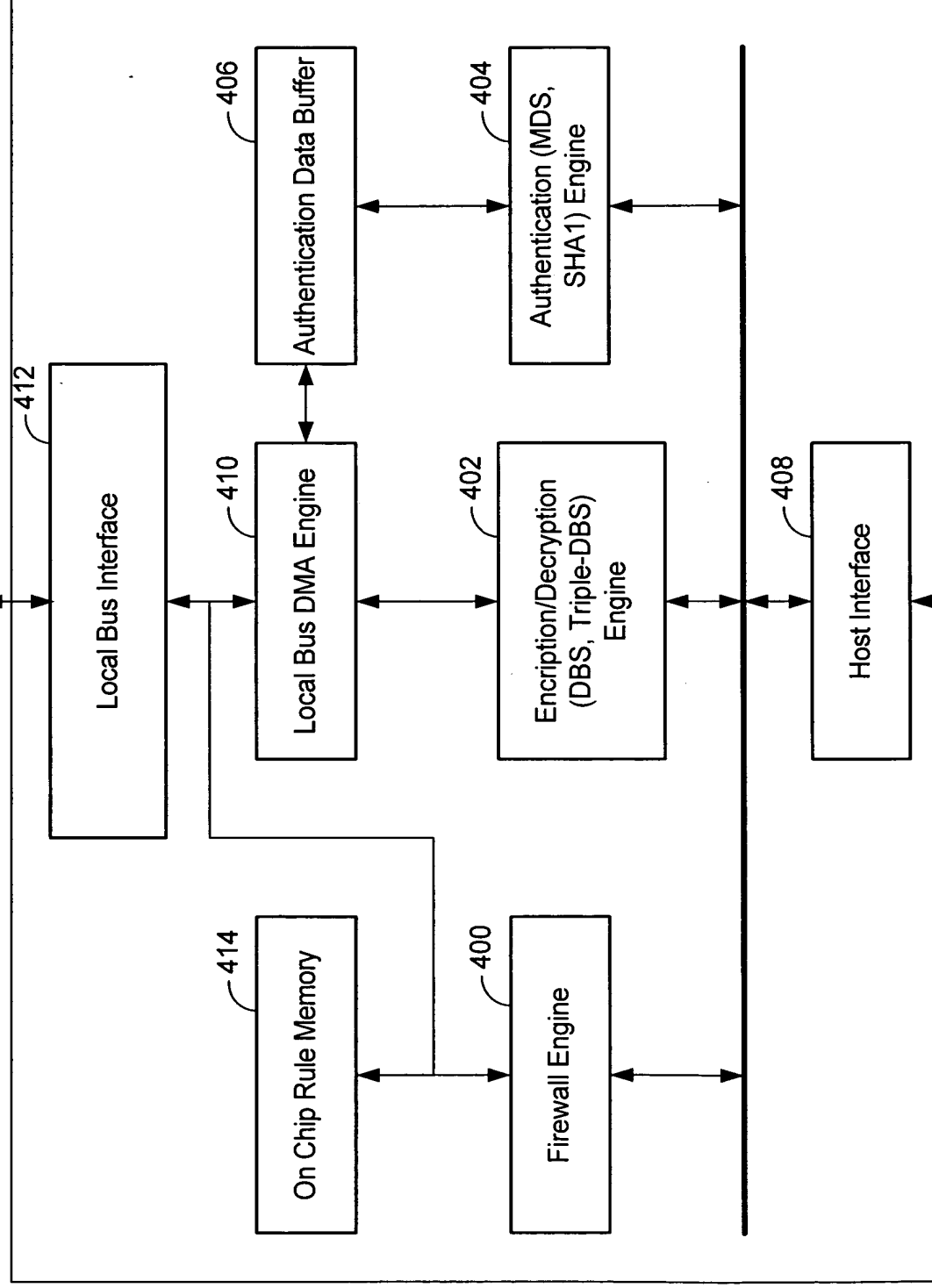


Fig.4

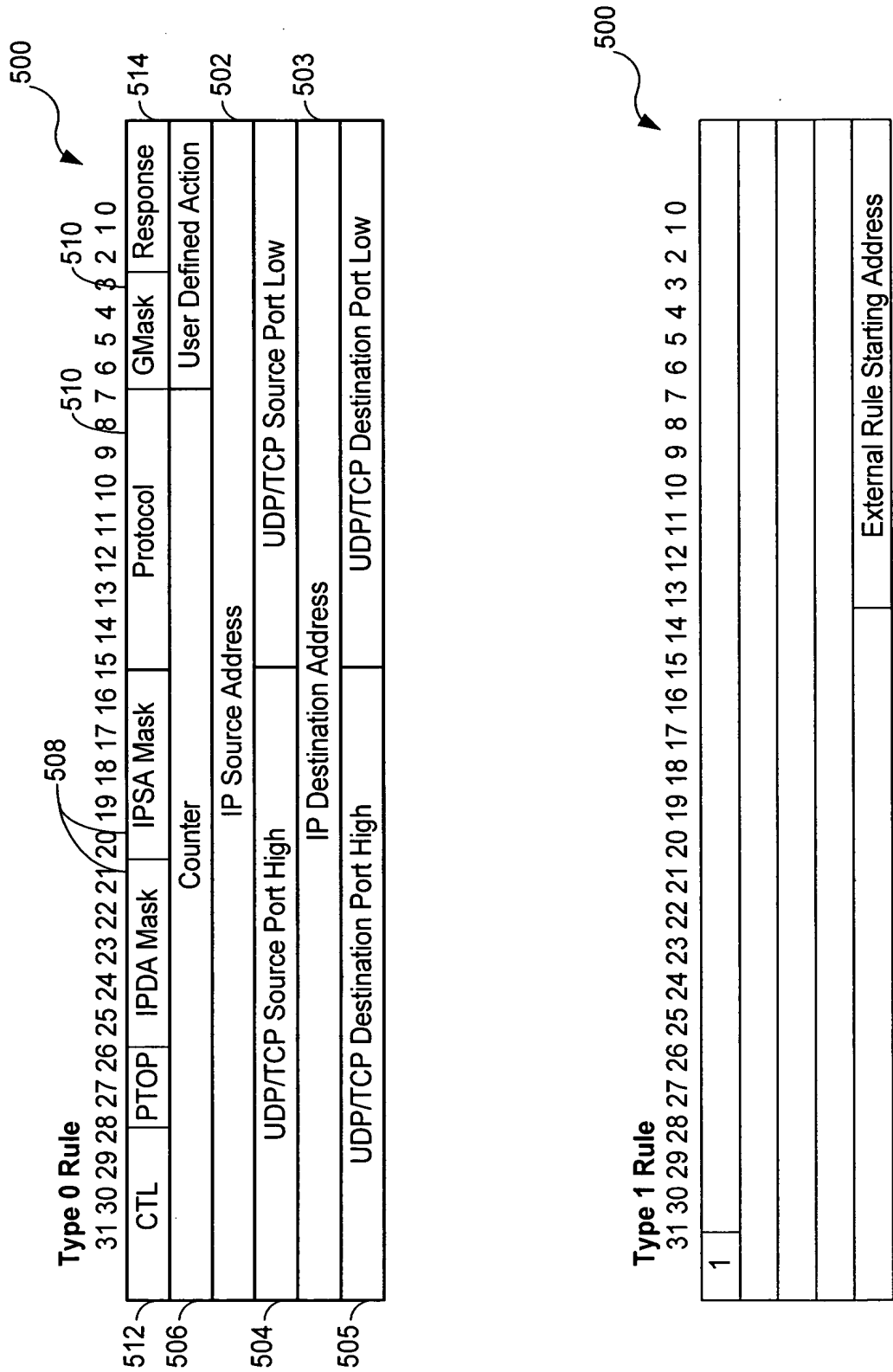
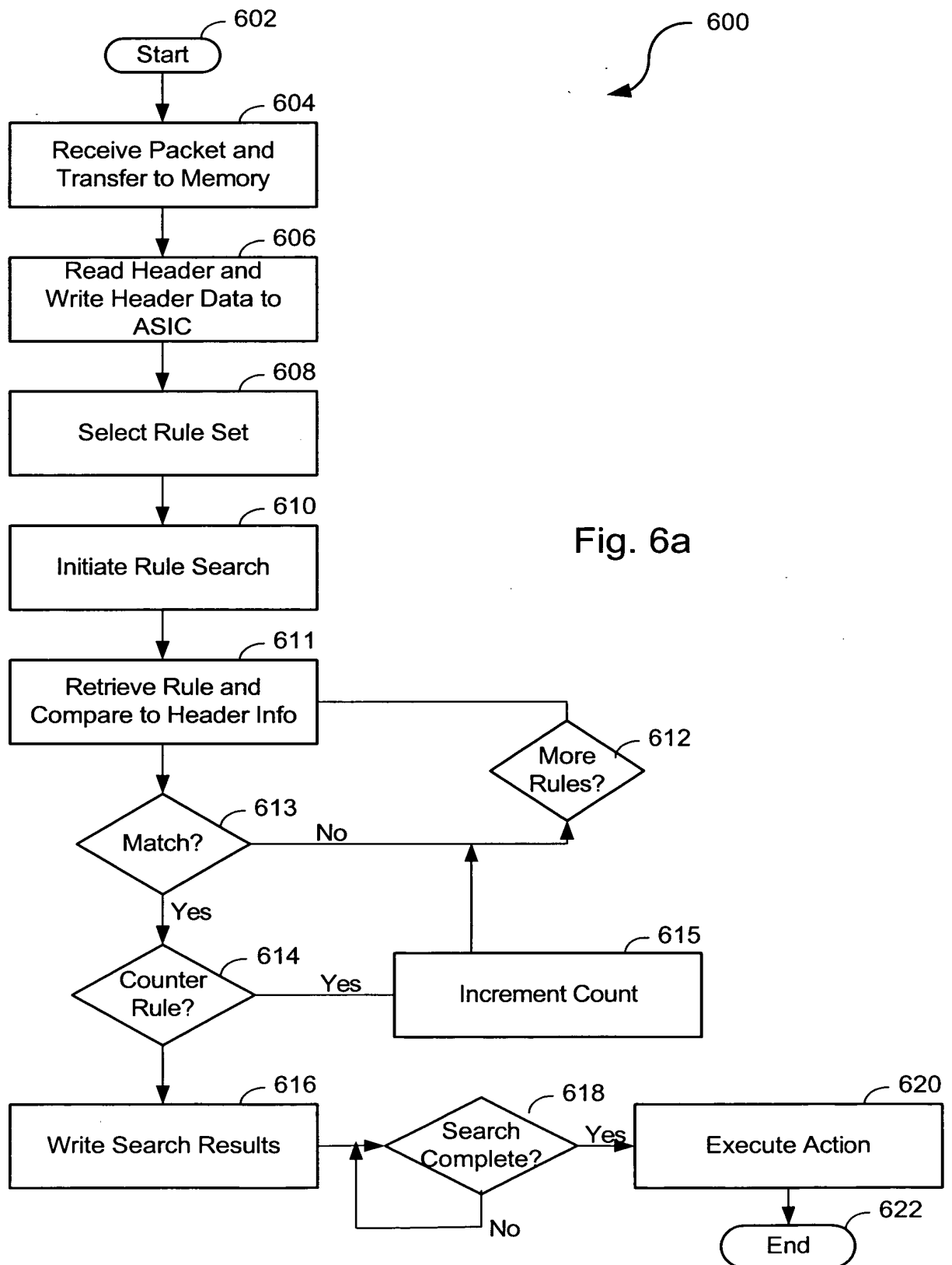


Fig. 5



	1 st Clock	2 nd Clock	3 rd Clock	4 th Clock	5 th Clock	6 th Clock	7 th Clock
1 st Rule	Fetch Rule	IP Comparison	Port Comparison				
2 nd Rule		Fetch Rule	IP Comparison	Port Comparison			
3 rd Rule			Fetch Rule	IP Comparison	Port Comparison		
4 th Rule				Fetch Rule	IP Comparison	Port Comparison	
5 th Rule					Fetch Rule	IP Comparison	Port Comparison

Fig. 6b

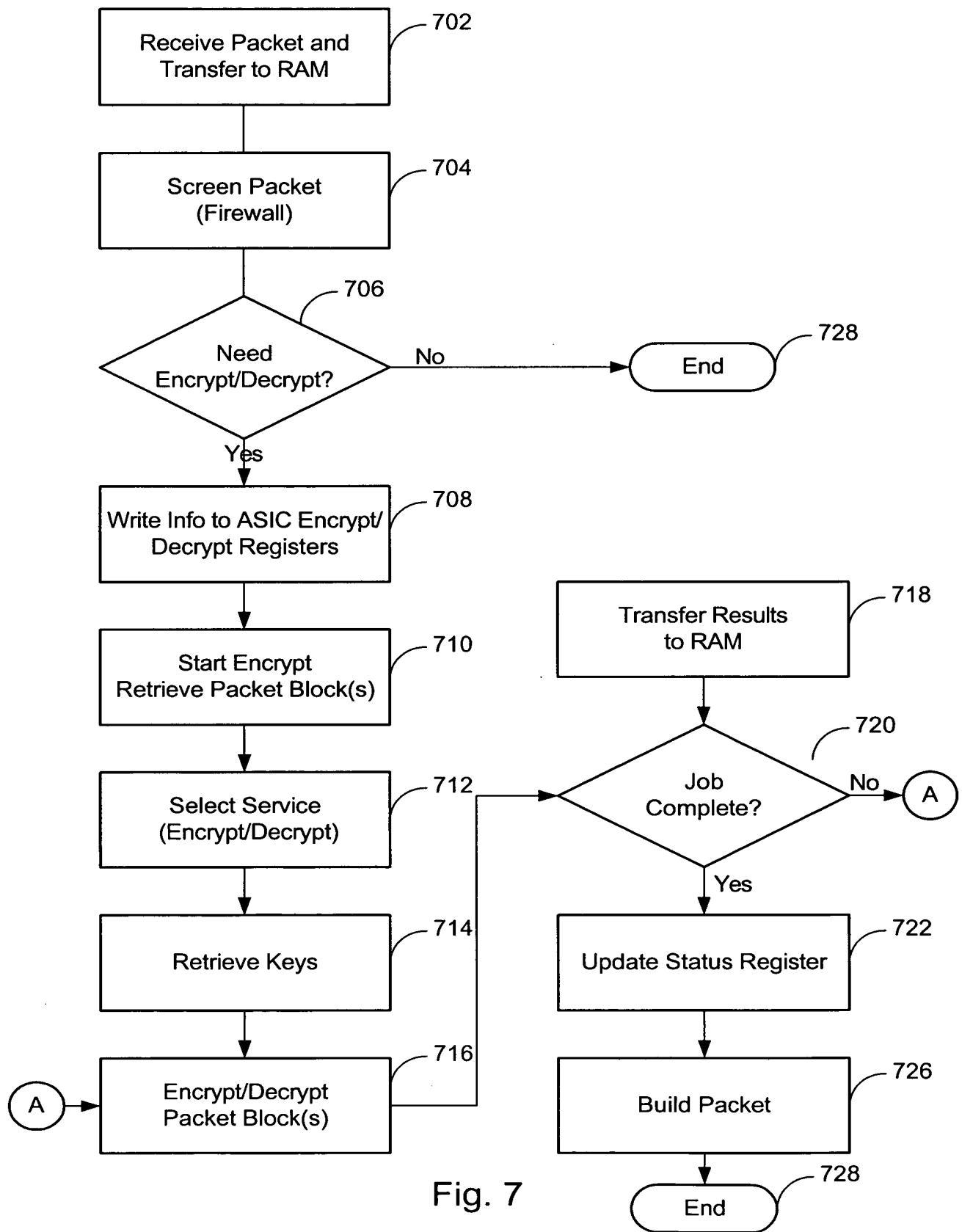


Fig. 7

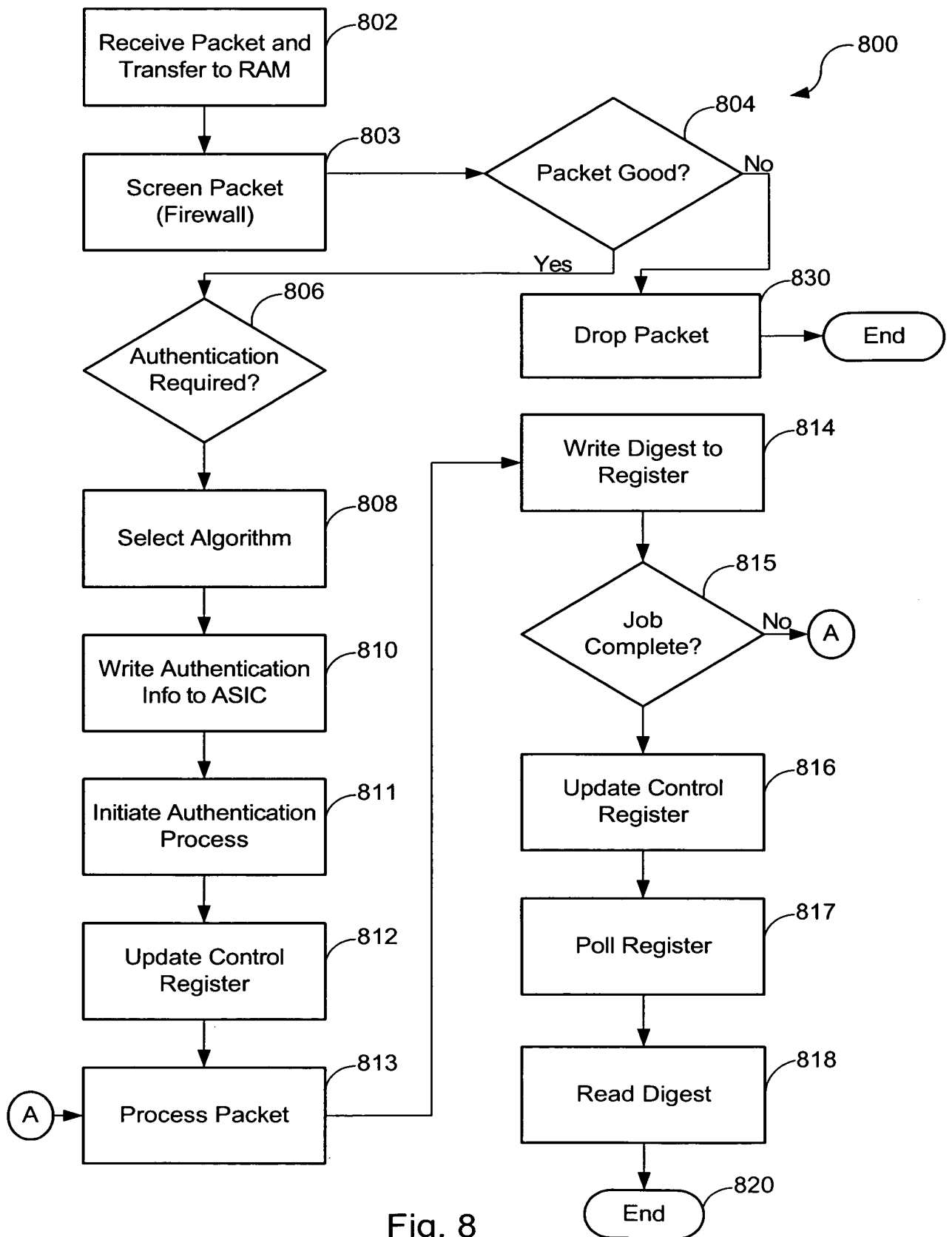


Fig. 8